**(Section 3)** **SD/Ethical Implications of follow-on products/markets**

RISC-V is a new, simple, open, and free instruction set architecture that can be freely used by any academic institution or business organization. With RISC-V, many new products can be produced, for example, using the RISC-V architecture to develop artificial intelligence chips for low-power devices, and providing lower-cost chips for intelligent driving.

RISC-V meets the Sustainable Development regulations, because it meets the Technocentric concerns, Eco-centric concerns and Sociocentric concern.

RISC-V is aimed at the Internet of Things market. With a budding market, RISC-V has its market opportunities. At this time, RISC-V is defined as a national standard instruction set by many countries, such as India. Furthermore, it has also attracted many companies to join the RISC-V Foundation and expressed support for it, including Samsung, Google, Huawei. This shows that RISC-V has good Eco-centric concerns.

The reason why RISC-V has become popular is that it has three advantages. Firstly, Minimalist. Compared with traditional commercial architectures, RISC-V has a streamlined length. Secondly, Clean. RISC-V clearly distinguishes between user and privileged instruction subsets, so it could reduce costs significantly. Thirdly, Stability. After several years of iteration, the benchmark instructions and some standard extensible instructions have been determined. These advantages indicate that RISC-V meets the technical needs of users and has great Technocentric concerns.

In addition, RISC-V allows commercial software release and sales based on open source code. In this situation, RISC-V gives users the opportunity to avoid ARM's high chip royalties and Intel x86 intellectual property system lock-up, so it greatly attracts those countries that promote an innovative economy and companies in the global chip industry. This shows that it has great Sociocentric concern.